

<b>Notice of References Cited</b>	Application/Control No. 10/090,257	Applicant(s)/Patent Under Reexamination BAZARGAN ET AL.	
	Examiner Gopal C. Ray	Art Unit 2111	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,625,681	09-2003	Erickson et al.	710/302
	B	US-6,401,157	06-2002	Nguyen et al.	710/302
	C	US-6,286,066	09-2001	Hayes et al.	710/302
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	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Experimental research on a hot swappable bus system" by Yamada, T.; Kaminaga, Y.; Kurosawa, K.; Ohashi, A.; Masui, K. (abstract only)
	V	"dynamic hardware plugins in an FPGA with partial run-time reconfiguration" by Horta, E.L.; Lockwood, J.W.; Taylor, D.E.; Parlour, D. (abstract only)
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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